

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,491
Filed: October 12, 2001
Inventor(s):
Michael C. Dorsey

Examiner: Trimmings, J.
Group/Art Unit: 2133
Atty. Dkt. No: 5681-55400

Title: ASIC BIST
CONTROLLER
EMPLOYING STORED
INDICATIONS OF
COMPLETION

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter
Printed Name
June 15, 2004
Date
Signature

SUBMISSION OF FORMAL DRAWINGS

Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

Erik A. Heter
Reg. No. 50,652
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Date: June 15, 2004

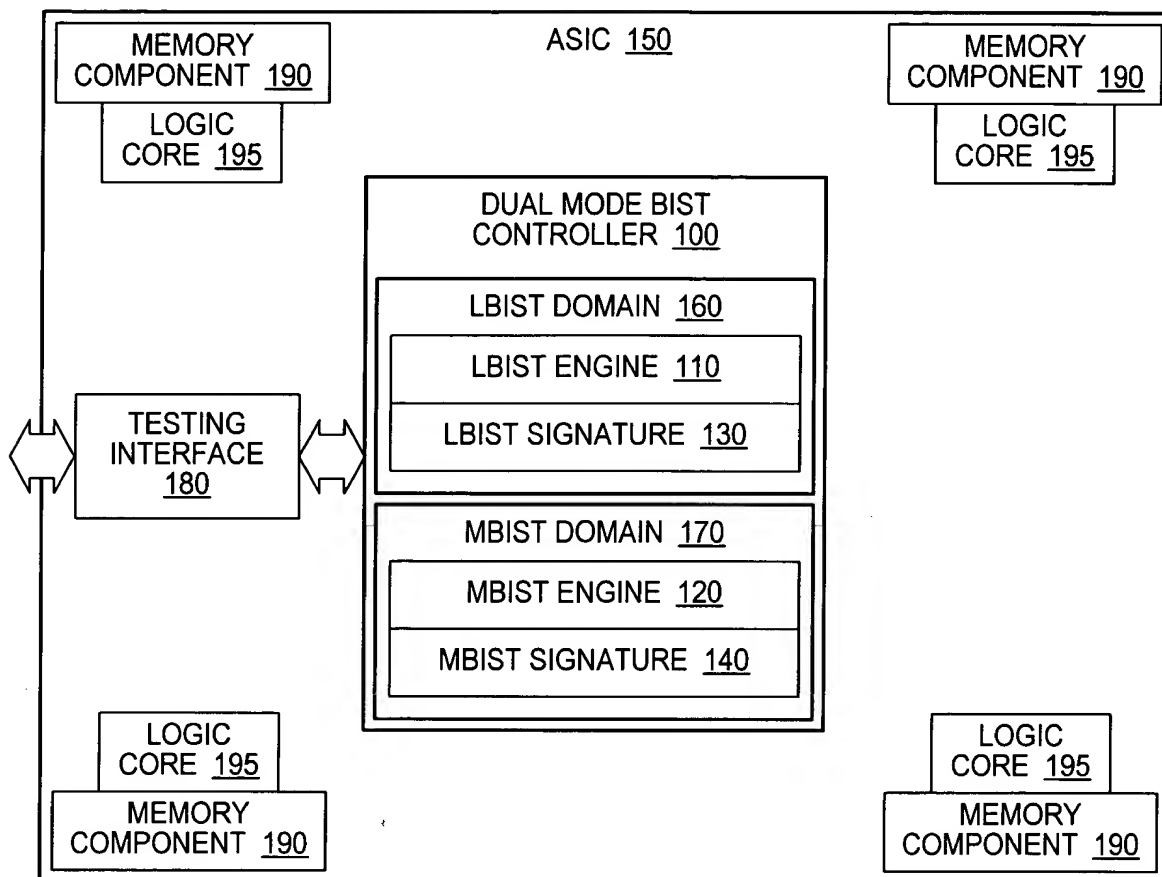
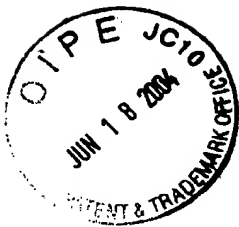


FIG. 1



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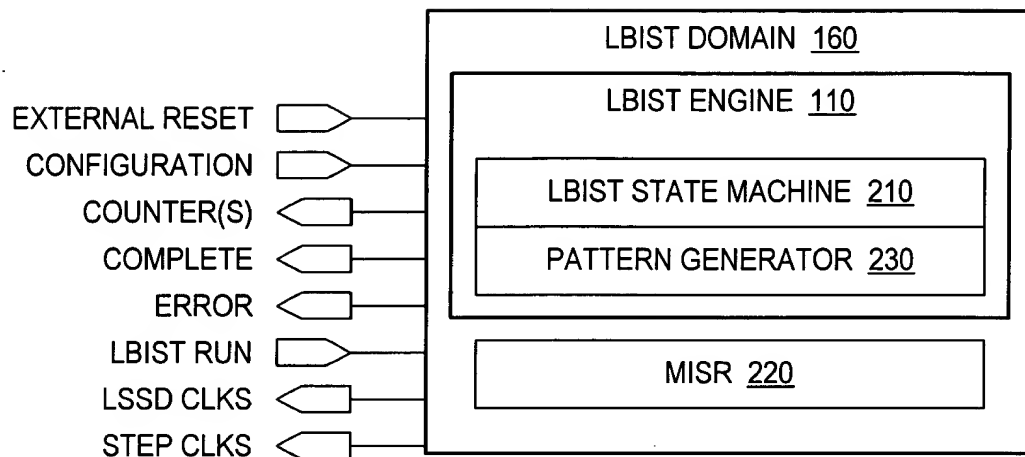


FIG. 2

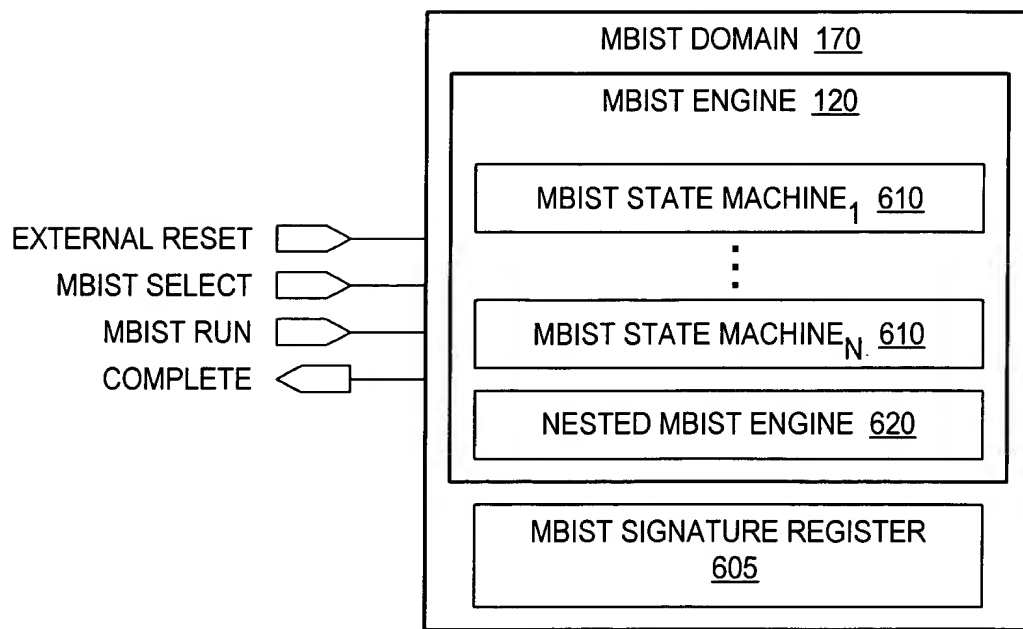


FIG. 6

210 →

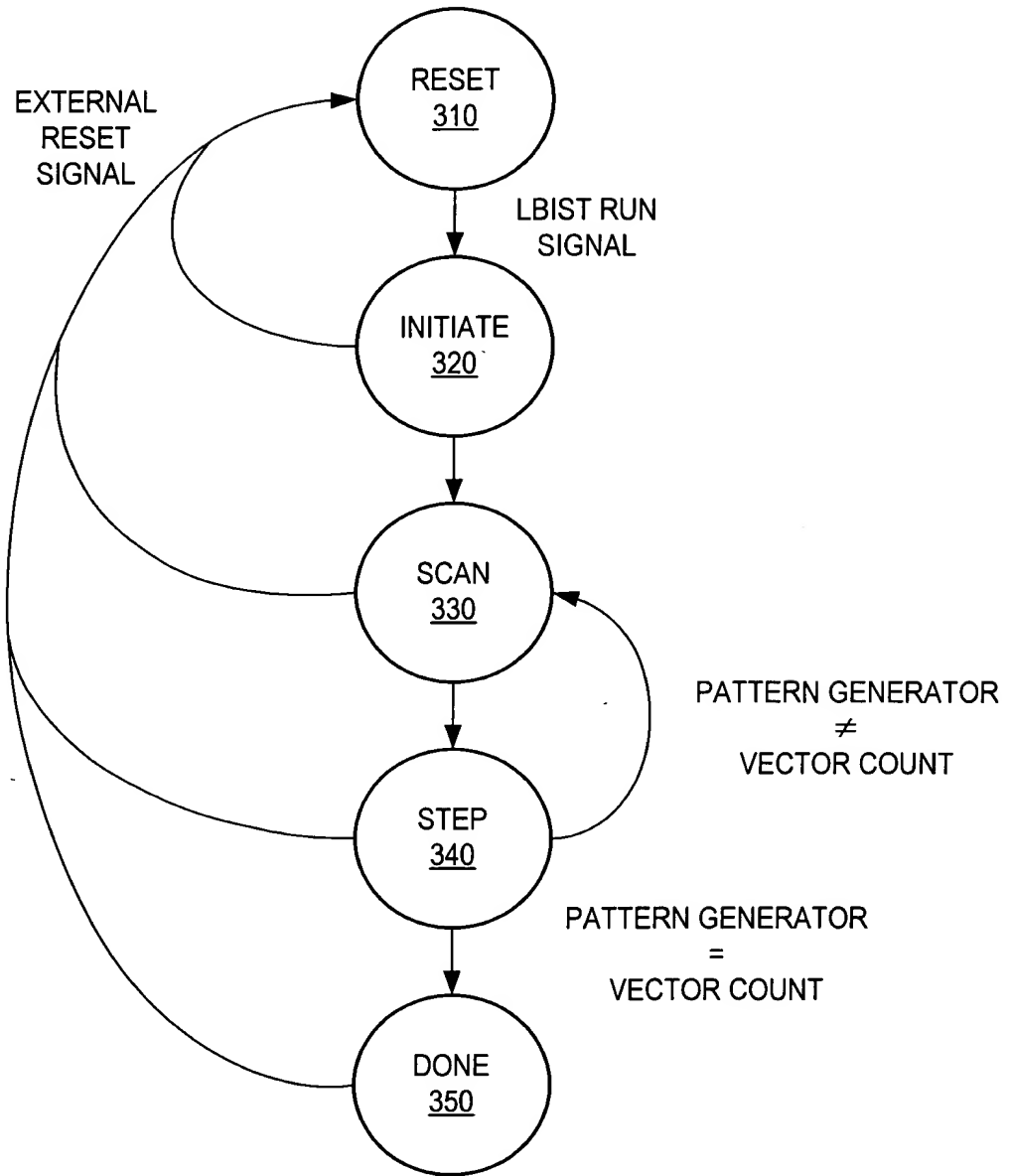
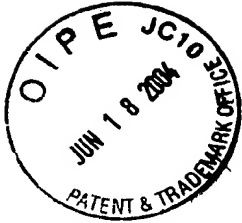


FIG. 3



B ₃₁	B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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FIG. 4

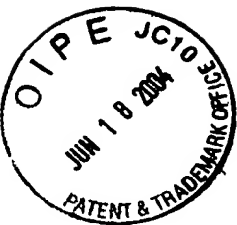
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B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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FIG. 5

B ₃₁	B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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FIG. 7



610 →

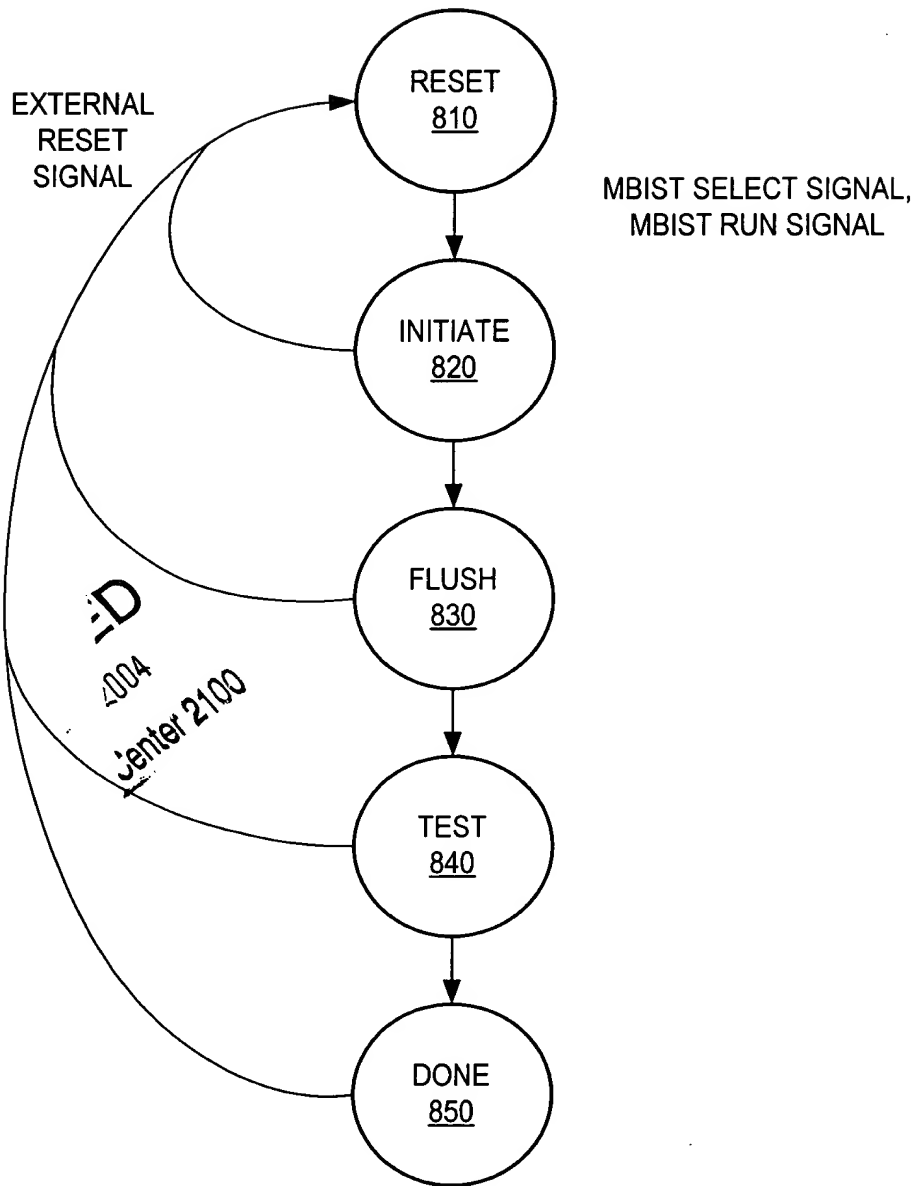
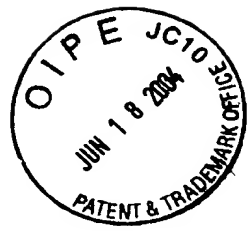


FIG. 8



Replacement Sheet

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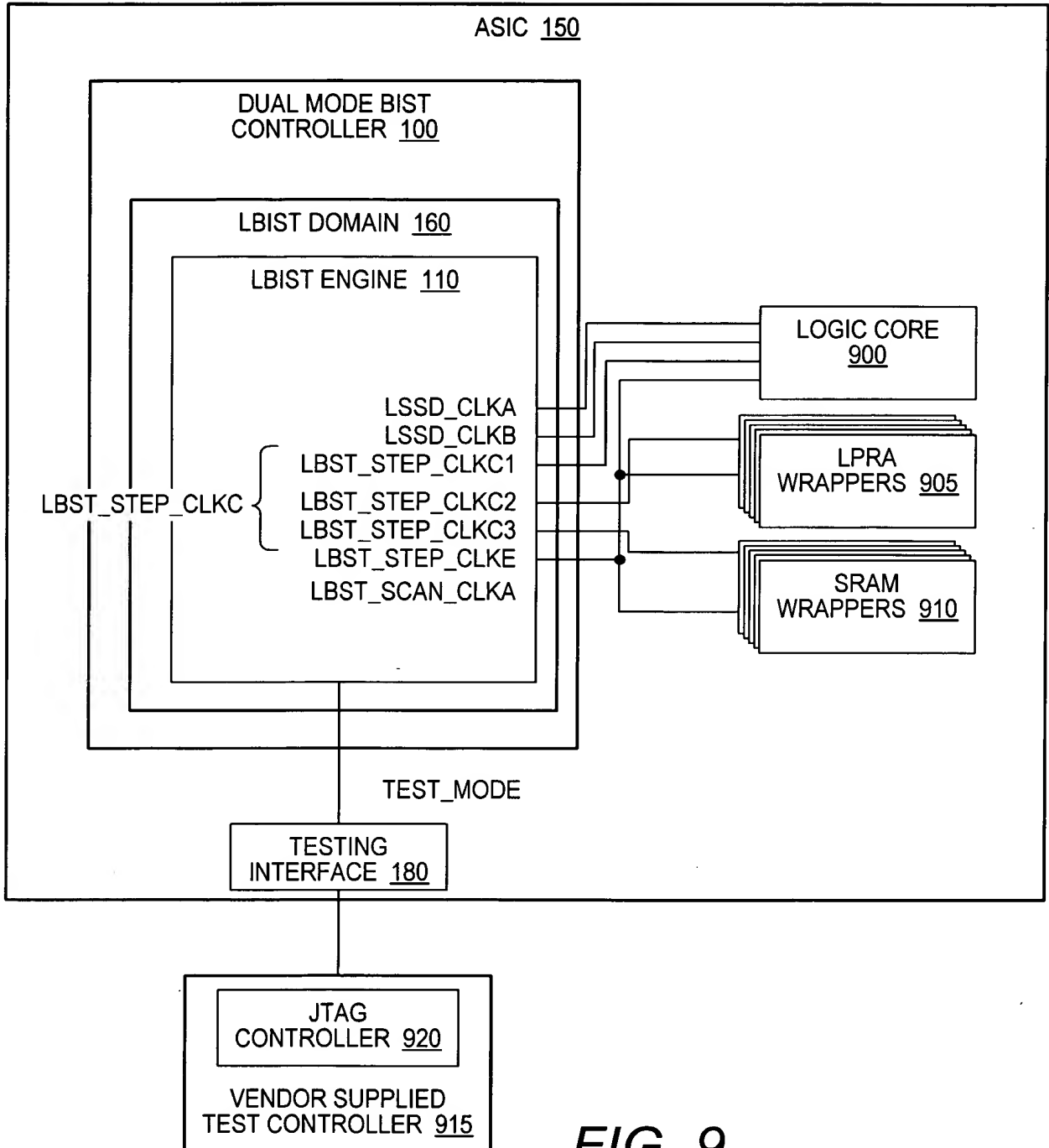


FIG. 9